

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09989777
Filing Date	2001-11-19
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Mary C. Jacob
Attorney Docket Number	CD01208M

1	USPTO Notice of Allowance for Application Number 10/001,477 (CD1203M) dated 05/08/2009; 6 pages	<input type="checkbox"/>
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Application Number	09989777
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Examiner Name	Mary C. Jacob
Attorney Docket Number	CD01208M

1	USPTO Final Rejection for 09/989,765 (CD01194M) dated 03/31/09; 18 pages	<input type="checkbox"/>
2	USPTO Final Rejection for 09/989,778 (CD01172) dated 03/16/09; 26 pages	<input type="checkbox"/>

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U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6385742	B1	2002-05-07	Kirsh et al.	Entire Document
	2	6810442	B1	2004-10-24	Lin et al.	Entire Document
	3	6487700	B1	2002-11-26	Fukushima	Entire Document
	4	6516428	B1	2003-02-04	Wenzel et al.	Entire Document
	5	6564179	B1	2003-05-13	Belhaj	Entire Document
	6	6581191	B1	2003-06-17	Schubert et al.	Entire Document
	7	6107769	B1	2000-08-22	Saylor et al.	Entire Document
	8	7073158	B1	2006-07-04	McCubbrey	Entire Document

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9	5241492	B1	1993-08-31	Girardeau Jr.	Entire Document
10	6445211	B1	2002-09-03	Saripella	Entire Document
11	6553057	B1	2003-04-22	Sha	Entire Document

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Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20040054821	A1	2004-03-18	Warren et al.	Entire Document

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FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	USPTO Application Number 09/975,105 (CD00184): "Host to FPGA Interface in an In-Circuit Emulation System," Craig Nemecek, filed on 10/10/2001; 44 pages	<input type="checkbox"/>

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2	USPTO Advisory Action for 09/994,601 dated 5/23/06 (CD01170M); 3 pages	<input type="checkbox"/>
3	USPTO Non-Final Rejection for 09/994,601 dated 7/29/04 (CD01170M); 10 pages	<input type="checkbox"/>
4	USPTO Ex Parte Quayle Action for 09/992,076 dated 06/18/07 (CD01210M); 6 pages	<input type="checkbox"/>
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	1	7188063	B1	2007-03-06	Snyder, Warren	Entire Document
	2	5329471	B1	1994-07-12	Swoboda, et al.	Entire Document
	3	5371878	B1	1994-12-06	Coker	Entire Document
	4	5852733	B1	1998-12-28	Chien, et al.	Entire Document
	5	7058921	B1	2006-06-06	Hwang et al.	Entire Document
	6	7024636	B1	2006-04-04	Weed	Entire Document
	7	7017145	B1	2006-03-21	Taylor	Entire Document
	8	7117485	B1	2006-10-03	Wilkinson et al.	Entire Document

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9	5978584	B1	1999-11-02	Nishibata et al.	Entire Document
10	6205574	B1	2001-03-20	Dellinger et al.	Entire Document
11	6732347	B1	2004-05-04	Camilleri et al.	Entire Document
12	6996799	B1	2006-02-07	Cismas et al.	Entire Document
13	7055035	B1	2006-05-30	Allison et al.	Entire Document
14	5544067	B1	1996-08-06	Rostoker et al.	Entire Document
15	6850554	B1	2005-02-01	Sha	Entire Document
16	7138841	B1	2006-11-21	Li	Entire Document
17	6542844	B1	2003-04-01	Hanna	Entire Document
18	6898703	B1	2005-05-24	Ogami et al.	Entire Document
19	6901563	B1	5005-05-31	Ogami et al.	Entire Document

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20	7086014	B1	2006-08-01	Bartz et al.	Entire Document
21	7150002	B1	2006-12-12	Anderson et al.	Entire Document
22	6338109	B1	1996-08-30	Snyder et al.	Entire Document
23	7406674	B1	2001-10-24	Ogami et al.	Entire Document
24	7103108	B1	2006-09-05	Beard	Entire Document
25	6434187	B1	2002-08-13	Beard	Entire Document
26	6140853	B1	2000-10-31	Lo	Entire Document
27	5889936	B1	1999-03-30	Chan	Entire Document
28	5745011	B1	1998-04-28	Scott	Entire Document
29	6784821	B1	2004-08-31	Lee, Soo-Hyoung	Entire Document
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31	7283151	B1	2007-10-16	Nihei et al.	Entire Document
32	5399922	B1	1995-03-21	Klani et al.	Entire Document
33	6718533	B1	2004-04-06	Schneider et al.	Entire Document
34	6611276	B1	2003-08-26	Muratori et al.	Entire Document
35	6823497	B1	2004-11-23	Schubert et al.	Entire Document
36	6865429	B1	2005-03-08	Schneider et al.	Entire Document
37	6664978	B1	2003-12-16	Kekic et al.	Entire Document
38	6601236	B1	2003-07-29	Curtis	Entire Document
39	5479643	B1	1995-12-26	Bhaskar et al.	Entire Document
40	5155836	B1	1992-10-13	Jordan et al.	Entire Document
41	6211741	B1	2001-04-03	Dalmia	Entire Document

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42	6272646	B1	2001-08-07	Rangasayee	Entire Document
43	6560306	B1	2003-05-06	Duffy	Entire Document
44	6667642	B1	2003-12-23	Moyal	Entire Document
45	6369660	B1	2002-04-09	Wei	Entire Document
46	6377646	B1	2002-04-23	Sha	Entire Document

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U.S. PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20050240917	A1	2005-10-27	Wu, Yuh-Cherng	Entire Document
	2	20030135842	A1	2003-07-17	Frey et al.	Entire Document
	3	20040205695	A1	2004-10-14	Fletcher, David	Entire Document
	4	20020174134	A1	2002-11-21	Goykhman	Entire Document

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1	Cypress MicroSystem, Inc. "Cypress Customer Forums" retrieved from <http://www.cypress.com/forums/messageview>; 11/30/2004; 1 page	<input type="checkbox"/>
2	Application Number 10/002,726 (CD01171M): "Method and Apparatus for Generating Microcontroller Configuration Information," Ogami et al., filed on 10/24/2001; 54 pages	<input type="checkbox"/>
3	USPTO Notice of Allowance for Application Number 10/002,726 (CD01171M) dated 02/06/2007; 7 pages	<input type="checkbox"/>
4	USPTO Non-Final Rejection for Application Number 10/002,726 (CD01171M) dated 08/28/2006; 10 pages	<input type="checkbox"/>
5	USPTO Final Rejection for Application Number 10/002,726 (CD01171M) dated 03/27/2006; 8 pages	<input type="checkbox"/>
6	USPTO Final Rejection for Application Number 10/002,726 (CD01171M) dated 11/30/2005; 10 pages	<input type="checkbox"/>
7	USPTO Non-Final Rejection for Application Number 10/002,726 (CD01171M) dated 06/10/2005; 10 pages	<input type="checkbox"/>
8	USPTO Non-Final Rejection for Application Number 10/002,726 (CD01171M) dated 12/13/2004; 7 pages	<input type="checkbox"/>
9	Application Number 11/818,005 (CD01171C): "Techniques for Generating Microcontroller Configuration Information," Ogami et al., filed on 6/12/2007; 61 pages	<input type="checkbox"/>
10	USPTO Requirement for Restriction/Election for Application Number 11/818,005 (CD01171C) dated 07/14/2009; 5 pages	<input type="checkbox"/>

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1	USPTO Advisory Action for Application Number 11/200,619 (CD04112) dated 05/11/2009; 3 pages	<input type="checkbox"/>
2	USPTO Final Rejection for Application Number 11/200,619 (CD04112) dated 03/03/2009; 14 pages	<input type="checkbox"/>
3	USPTO Non-Final Rejection for Application Number 11/200,619 (CD04112) dated 08/27/2008; 13 pages	<input type="checkbox"/>
4	Application Number 11/200,619 (CD04112): "Providing hardware independence to automate code generation of processing device firmware," Snyder et al.; filed on 08/10/2005; 41 pages	<input type="checkbox"/>
5	USPTO Notice of Allowance for Application Number 11/201,922 (CD04111) dated 04/09/2009; 4 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/21/2008; 12 pages	<input type="checkbox"/>
7	USPTO Final Rejection for Application Number 11/201,922 (CD04111) dated 04/30/2008; 10 pages	<input type="checkbox"/>
8	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/15/2007; 10 pages	<input type="checkbox"/>
9	Application Number 11/201,922 (CD04111): "Design model for a hardware device-independent method of defining embedded firmware for programmable systems," McDonald et al.; filed on 08/10/2005; 31 pages	<input type="checkbox"/>
10	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 12/12/2008; 17 pages	<input type="checkbox"/>
11	USPTO Final Rejection for Application Number 11/201,627 (CD04109) dated 04/29/2008; 21 pages	<input type="checkbox"/>

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12	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 11/16/2007; 16 pages	<input type="checkbox"/>
13	Application Number 11/201,627 (CD04109): "Method and an apparatus to design a processing system using a graphical user interface," Ogami et al.; filed on 08/10/2005; 37 pages	<input type="checkbox"/>
14	USPTO Notice of Allowance for Application Number 09/989,808 (CD01169M) dated 02/13/2006; 4 pages	<input type="checkbox"/>
15	USPTO Non-Final Rejection for Application Number 09/989,808 (CD01169M) dated 10/19/2005; 8 pages	<input type="checkbox"/>
16	USPTO Non-Final Rejection for Application Number 09/989,808 (CD01169M) dated 04/14/2005; 8 pages	<input type="checkbox"/>
17	Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al., filed on 11/19/2001; 67 pages	<input type="checkbox"/>
18	USPTO Notice of Allowance for Application Number 10/109,979 (CD01211M) dated 03/14/2006; 6 pages	<input type="checkbox"/>
19	USPTO Non-Final Rejection for Application Number 10/109,979 (CD01211M) dated 06/30/2005; 6 pages	<input type="checkbox"/>
20	Application Number 10/109,979 (CD01211M): "Graphical user interface with logic unifying functions," Anderson et al., filed on 03/29/2002; 100 pages	<input type="checkbox"/>
21	Application Number 09/979,781 (CD01180M): "System and method for decoupling and iterating resources associated with a module," Ogami et al., filed on 11/27/2001; 40 pages	<input type="checkbox"/>
22	Application Number 09/989,775 (CD01195M): "User defined names for registers in memory banks derived from configurations," Ogami et al., filed on 11/19/2001; 29 pages	<input type="checkbox"/>

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23	USPTO Notice of Allowance for Application Number 09/989,819 (CD01181M) dated 01/11/2005; 7 pages	<input type="checkbox"/>
24	USPTO Non-Final Rejection for Application Number 09/989,819 (CD01181M) dated 07/13/2004; 4 pages	<input type="checkbox"/>
25	USPTO Miscellaneous Action for Application Number 09/989,819 (CD01181M) dated 12/14/2001; 1 page	<input type="checkbox"/>
26	Application Number 09/989,819 (CD01181M): "System and method for creating a boot file utilizing a boot template," Ogami et al., filed on 11/19/2001; 43 pages	<input type="checkbox"/>
27	USPTO Notice of Allowance for Application Number 09/989,761 (CD01179M) dated 01/14/2005; 6 pages	<input type="checkbox"/>
28	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 08/26/2004; 7 pages	<input type="checkbox"/>
29	USPTO Non-Final Rejection for Application Number 09/989,761 (CD01179M) dated 03/10/2004; 6 pages	<input type="checkbox"/>
30	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 10/03/2003; 7 pages	<input type="checkbox"/>
31	USPTO Non-Final Rejection for Application Number 09/989,761 (CD01179M) dated 04/18/2003; 5 pages	<input type="checkbox"/>
32	Application Number 09/989,761 (CD01179M): "Storing of global parameter defaults and using them over two or more design projects," Ogami et al., filed on 11/19/2001; 37 pages	<input type="checkbox"/>
33	Wang, et al. "Synthesizing Operating System Based Device Drivers in Embedded Systems," 2003, ACM; 8 pages	<input type="checkbox"/>

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34	Lutovac et al. "Symbolic Computation of Digital Filter Transfer Function Using MATLAB," Proceedings of 23rd International Conference on Microelectronics (MIEL 2002), Vol. 2 NIS, Yugoslavia; 4 pages	<input type="checkbox"/>
35	Nouta et al. "Design and FPGA-Implementation of Wave Digital Bandpass Filters with Arbitrary Amplitude Transfer Characteristics," Proceedings of IEEE International Symposium on Industrial Electronics; 1998, Vol. 2; 5 pages	<input type="checkbox"/>
36	Xilinx, Virtex-II Pro Platform FPGA Developer's Kit, "How Data2BRAM Fits in with Hardware and Software Flows," Chapter 2: Using Data2BRAM; January 2003 Release; 2 pages	<input type="checkbox"/>
37	PCT Preliminary Report on Patentability (Chapter 1 of the Patent Cooperation Treaty), PCT/US2005/028793, filed 08/12/2005, mailed 12/21/2007; 2 pages	<input type="checkbox"/>
38	PCT Written Opinion of the International Searching Authority for PCT/US2005/028793, filed 8/12/2005, mailed 11/19/2007; 7 pages	<input type="checkbox"/>
39	PCT International Search Report of the International Searching Authority for PCT/US05/28793, filed 08/12/2005, mailed 11/19/2007; 5 pages	<input type="checkbox"/>
40	International Search Report and Written Opinion of the International Searching Authority for PCT/US05/28898, filed 08/12/2005, mailed 03/06/2007; 6 pages	<input type="checkbox"/>
41	Burogs et al., "Power Converter Analysis and Design using Matlab: A Transfer Function Approach," Proceedings of IEEE International Symposium on Industrial Electronics 1998, Vol. 2; 6 pages	<input type="checkbox"/>
42	Efstathiou, "Analog Electronics: Basic Circuits of Operational Amplifiers," < http://web.archive.org/web/20021231045232/ > December 31, 2002, version, retrieved from the Internet Archives; 10 pages	<input type="checkbox"/>
43	PCT International Search Report for PCT/US05/28791, filed 08/12/2005, mailed 3/31/2008; 4 pages	<input type="checkbox"/>
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	1	5708798	B1	1998-01-13	Lynch et al.	Entire Document
	2	6825869	B1	2004-11-30	Bang	Entire Document
	3	4942540	B1	1990-07-17	Black et al.	Entire Document
	4	5394522	B1	1995-02-28	Sanchez-Frank et al.	Entire Document
	5	5867399	B1	1999-02-02	Rostoker et al.	Entire Document
	6	5930150	B1	1999-07-27	Cohen et al.	Entire Document
	7	5933356	B1	1999-08-03	Rostoker et al.	Entire Document
	8	5941991	B1	1999-08-24	Kageshima	Entire Document

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10	6366300	B1	2002-04-02	Ohara et al.	Entire Document
11	6388109	B1	2002-05-14	Shwarz et al.	Entire Document
12	6457479	B1	2002-10-01	Zhuang et al.	Entire Document
13	6571331	B1	2003-05-27	Henry et al.	Entire Document
14	6594799	B1	2003-07-15	Chiang et al.	Entire Document
15	6750889	B1	2004-06-15	Livingston et al.	Entire Document
16	6002268	B1	1999-12-14	Sasaki et al.	Entire Document
17	6864710	B1	2005-03-08	Lacey et al.	Entire Document
18	5341267	B1	1994-08-23	Whitten	Entire Document
19	6271679	B1	2001-08-07	McClintock et al.	Entire Document

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20	4878200	B1	1989-10-31	Asghar et al.	Entire Document
21	5136188	B1	1992-08-04	Ha et al.	Entire Document
22	5280202	B1	1994-01-18	Chan et al.	Entire Document
23	5384745	B1	1995-01-24	Konishi et al.	Entire Document
24	5422823	B1	1995-06-06	Agrawal et al.	Entire Document
25	5521529	B1	1996-05-28	Agrawal et al.	Entire Document
26	5451887	B1	1995-09-19	El-Avat et al.	Entire Document
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29	5781030	B1	1998-07-14	Agrawal et al.	Entire Document
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33	5848285	B1	1998-12-08	Kapusta et al.	Entire Document
34	5966027	B1	1999-10-12	Kapusta et al.	Entire Document
35	6049223	B1	2000-04-11	Lytle et al.	Entire Document
36	6058452	B1	2000-05-02	Rangasayee et al.	Entire Document
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39	6191998	B1	2001-02-20	Reddy et al.	Entire Document
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Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20080186052	A1	2008-08-07	Needham et al.	Entire Document
	2	20070139074	A1	2007-06-21	Reblewski, Frederic	Entire Document

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	1	Kory Hopkins, "Definition;" 01/16/1997; < http://www.cs.sfu.ca/cs/people/GradStudent.html >; 1 page	<input type="checkbox"/>
	2	Ebeling et al., "Validating VLSI Circuit Layout by Wirelist Comparison;" Sept. 1983; in proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-83); 2 pages	<input type="checkbox"/>
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8	Written Opinion of the International Search Authority for International Application Number PCT/US08/60680 (CD2007130PCT) dated 08/15/2008; 4 pages	<input type="checkbox"/>
9	USPTO Notice of Allowance for Application Number 09/475,879 (CD99047) dated 10/22/2004; 7 pages	<input type="checkbox"/>
10	USPTO Advisory Action for Application Number 09/475,879 (CD99047) dated 03/04/2002; 3 pages	<input type="checkbox"/>
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13	USPTO Non-Final Rejection for Application Number 09/475,879 (CD99047) dated 03/08/2001; 6 pages	<input type="checkbox"/>
14	USPTO Application Number 09/475,879 (CD99047): "Programmable Logic Device," Lacey et al.; filed on 12/30/1999; 50 pages	<input type="checkbox"/>
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16	USPTO Non-Final Rejection for Application Number 09/475,808 (CD99069) dated 06/06/2001; 8 pages	<input type="checkbox"/>
17	USPTO Notice of Allowance for Application Number 09/475,808 (CD99069) dated 11/06/2001; 8 pages	<input type="checkbox"/>
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19	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 11/05/2004; 17 pages	<input type="checkbox"/>
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25	USPTO Notice of Allowance for Application Number 10/137,497 (CD01197M) dated 07/20/2007; 4 pages	<input type="checkbox"/>
26	Application Number 10/653,050 (CD01242C): "Method and System for Programming a Memory Device," Snyder et al.; filed on 08/29/2003; 69 pages	<input type="checkbox"/>
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	1	7256588	B1	2007-08-14	Howard et al.	Entire Document
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9	6661410	B1	2003-12-09	Casebolt et al.	Entire Document
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14	5805897	B1	1998-09-08	Glowny, David Andrew	Entire Document
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20	5646544	B1	1997-07-08	Iadanza, Joseph Andrew	Entire Document
21	5384910	B1	1995-01-24	Torres, Robert J.	Entire Document
22	5479603	B1	1995-12-26	Stone et al.	Entire Document
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31	5430734	B1	1995-07-04	Gilson, Kent L.	Entire Document
32	6332137	B1	2001-12-18	Hori et al.	Entire Document
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42	5917356	B1	1999-06-29	Casal et al.	Entire Document
43	7295049	B1	2007-11-13	Moyal et al.	Entire Document
44	5491458	B1	1996-02-13	McCune	Entire Document
45	5651035	B1	1997-07-22	Tozun	Entire Document
46	5886582	B1	1999-03-23	Stansell	Entire Document
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	2	20030097640	A1	2003-05-22	Abrams et al.	Entire Document
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	1	USPTO Application Number 11/273,708 (CD05141): "Capacitance Sensor Using Relaxation Oscillators," Snyder et al., filed on 11/14/2005; 33 pages	<input type="checkbox"/>
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	4	USPTO Notice of Allowance for Application Number 11/273,708 (CD05141) dated 08/09/2007; 4 pages	<input type="checkbox"/>
	5	USPTO Application Number 11/337,272 (CD05191): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 01/20/2006 29 pages	<input type="checkbox"/>
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12	USPTO Application Number 11/983,291 (CD05191D): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 11/07/2007; 26 pages	<input type="checkbox"/>
13	USPTO Non-Final Rejection for Application Number 11/983,291 (CD05191D) dated 03/09/2009; 9 pages	<input type="checkbox"/>
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19	USPTO Notice of Allowance for Application Number 11/709,866 (CD05170) dated 04/07/2009; 8 pages	<input type="checkbox"/>
20	Sedra et al., "Microelectronic Circuits," 3rd Edition, 1991, Oxford University Press; 20 pages	<input type="checkbox"/>
21	Van Ess, David; "Simulating a 555 Timer with PSoC," Cypress Semiconductor Corporation, Application Note AN2286, May 19, 2005; 10 pages	<input type="checkbox"/>
22	Cypress Semiconductor Corporation, "FAN Controller CG6457AM and CG6462AM," PSoC Mixed Signal Array Preliminary Data Sheet; May 24, 2005; 25 pages	<input type="checkbox"/>
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25	Chapweske, Adam; "The PS/2 Mouse Interface," PS/2 Mouse Interfacing, 2001; 11 pages	<input type="checkbox"/>
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27	Seguine, Ryan; "Layout Guidelines for PSoC CapSense," Cypress Semiconductor Corporation, Application Note AN2292; July 22, 2005; 13 pages	<input type="checkbox"/>
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30	Cypress Semiconductor Corporation, "PSoC CY8C20x34 Technical Reference Manual (TRM)," PSoC CY8C20x34 TRM, Version 1.0, 2006; 220 pages	<input type="checkbox"/>
31	USPTO Application Number 11/166,622 (CD05027): "Touch wake for electronic devices," Beard et al., filed on 06/23/2005; 22 pages	<input type="checkbox"/>
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33	International Search Report for International Application No. PCT/US2006/09572 dated 01/10/2008; 2 pages	<input type="checkbox"/>
34	USPTO Notice of Allowance for Application Number 10/024,093 (CD99069C) dated 09/10/2002; 7 pages	<input type="checkbox"/>
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	Filing Date	2001-11-19
	First Named Inventor	Craig Nemecek
	Art Unit	2123
	Examiner Name	Mary C. Jacob
	Attorney Docket Number	CD01208M

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